## COMBINED DECLARATION AND POWER OF ATTORNEY

As the below named inventor, I hereby declare that:

the specification of which

My residence, post office address and citizenship are as stated below next to my name and that I believe I am an original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

## METHOD FOR FABRICATING TRANSISTOR HAVING FULLY SILICIDED GATE

X is attached hereto.					
was filed on as Application Serial No		and was amended on			
specification, including the cl I acknowledge the duty application in accordance with	aims, as amended by y to disclose informa h Title 37, Code of Fe riority benefits under inventor's certificate	any amendment referration which is material ederal Regulations, § 7 Title 35, United States listed below and have	to the patentability of this I.56(a). Code, § 119 of any foreign e also identified below any		
	Not Applicabl	le			
I hereby appoint the fo transact all business in the P			cute this application and to with:		
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## COMBINED DECLARATION AND POWER OF ATTORNEY CONTINUED

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

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